



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS: Currie *et al.* **CONFIRMATION NO.:** 7878
SERIAL NO.: 10/696,994 **GROUP NO.:** 2823
FILING DATE: October 30, 2003 **EXAMINER:** Brewster, W.
TITLE: METHODS FOR PRESERVING STRAINED SEMICONDUCTOR
SUBSTRATE LAYERS DURING CMOS PROCESSING

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450


SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

In accordance with the provisions of 37 C.F.R. 1.97 and 1.98, Applicants hereby make of record the U.S. patent listed on the accompanying Form PTO-1449, for consideration by the Examiner in connection with the examination of the above-identified patent application. This statement is being filed before the mailing of a first Office action on the merits after the filing of a request for continued examination under 37 C.F.R. 1.114

It is respectfully requested that the patent listed on the attached Form PTO-1449 be made of record in this application.

Respectfully submitted,

Date: January 25, 2006



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FORM PTO - 1449 SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT					ATTORNEY DOCKET NO.: ASC-063 APPLICANT(S): Currie <i>et al.</i> SERIAL NO.: 10/696,994 FILING DATE: October 30, 2003 GROUP: 2823				
U.S. PATENT DOCUMENTS									
EXAM. INIT.		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE		
	A9	5,565,690	10/15/96	Theodore <i>et al.</i>					
FOREIGN PATENT DOCUMENTS									
EXAM. INIT.		DOCUMENT NUMBER	DATE	COUNTRY CODE	CLASS	SUB CLASS	FILING DATE	ABSTRACT ONLY	ENGLISH LANG (Y/N)
OTHER ART, JOURNAL ARTICLES, ETC.									
EXAM. INIT.	OTHER DOCUMENTS: (Including Author, Title, Date, Relevant Pages, Place of Publication)								
EXAMINER					DATE CONSIDERED				

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